

ABSTRACT

Systems, methodologies, components, and other embodiments associated with converting power and bus architectures are described. One exemplary system embodiment comprises a first set of power converters configured to convert a received input power level to one or more power levels, and a second set of power converters. An interleaved intermediate bus is configured to supply independent and redundant input to the second set of power converters from the one or more output power levels of the first set of power converters.